

(Use several sheets if necessary)

Application No. **07-58886U**
NEW NATIONAL PHASE

Filing Date:
August 9, 2006

Group Art Unit:

[illegible]

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
JC/	103 47 975	3/15/2004	GERMANY				
JC/	198 43 640	3/30/2000	GERMANY				

JC/	Rechenberg, Pomberger: "Informatik-Hanbuch, 3. Auflage", 2002, Hanser, Muenchen, Seite 275
↓	C. Wiegand, C. Siemers, H. Richter: "Definition of a Configurable Architecture for Implementation of Global Cellular Automation", Bd. 2981, Febr. 2004, Seiten 140-155
↓	L. Jozwiak, et al: "Effective and efficient FPGA synthesis through general functional decomposition" Journal of Systems Architecture, Elsevier, Science Publishers BV, Amsterdam, NL, Bd. 49, No. 4-6, Sept. 2003, Seiten 247-265
↓	M.M. Mirsalehi, T.K. Gaylord: "Logical minimization of multilevel coded functions", Applied Optics, Bd. 25, No. 18, Sept. 1986, Seiten 3078-3088
↓	R. Hoffmann, K.O. Volkmann, W. Heenes: "Globaler Zellularautomat (GCA): Ein neues massivparalleles Berechnungsmodell" PARS WORKSHOP, Oktober 2001
JC/	R. Hoffmann et al: "GCA: a massively parallel model", Parallel and distributed processing symposium, 2003. Proceedings international, April 2003, Piscataway, NJ, USA, Seiten 270-276

DATE CONSIDERED 08/29/2007

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

* English language abstract provided for the Examiner's convenience

ldw



**INFORMATION DISCLOSURE CITATION
IN AN APPLICATION**

(Use several sheets if necessary)

Attorney Docket No.:

4001-1222

Application No.:

10/588,860

Applicant:

Christian SIEMERS et al.

Filing Date:

August 9, 2006

Group Art Unit:

unknown

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

/JC/		Macias, N.J.: Ring around the PIG: a parallel GA with only local interactions coupled with a self-reconfigurable hardware platform to implement an O(1) evolutionary cycle for evolvable hardware, IEEE-A1, Vol. 2, 1999
/JC/		Macias, N.J.: The PIG paradigm: the design and use of a massively parallel fine grained self-reconfigurable infinitely scalable architecture, IEEE-A1, pages 175-180, 1999

EXAMINER: /James Cho/

DATE CONSIDERED 08/29/2007

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

* Abstract provided for the Examiner's convenience